

### **REMARKS**

Claims 1 - 24 are pending. Claims 1, 2, 4-10, 12-14 and 16-19 stand rejected. Claims 3, 11 and 15 are allowed.

Claims 1, 3, 8-12 and 16-24 have been amended. No new matter has been added.

Claims 9-10, 12, 16-19 and 20-24 stand rejected under 35 USC §112, second paragraph, as being indefinite for failing to point out and distinctly claim the subject matter regarded as the invention.

Applicant thanks the examiner for his observations and has amended the claims to correct the errors noted and more clearly state the invention.

Having amended the claims, applicant believes that the reason for the rejection has been overcome. Applicant respectfully requests that the rejection be withdrawn and the claims allowed.

Claims 20-24 are rejected under 35 USC §112, first paragraph because the specification does not reasonably provide enablement for a data processing unit comprising a second data processing unit. "The specification only discloses a data processing unit compris[ing] a first processor and a video controller."

Applicant respectfully disagrees with, and explicitly traverses, the reason for rejecting the claims. However, in the interest of advancing the prosecution of this matter, applicant has elected to amend the claims to recite a data processing configuration comprising a first processor and a video controller.

Having amended the claims, applicant believes that the reason for the rejection has been overcome. Applicant respectfully requests that the rejection be withdrawn and the claims allowed.

Claims 1, 2, 4-6, 13, 14 and 16-18 stand rejected under 35 USC §102(b) as being anticipated by Carmean (USP No. 5,669,003).

Applicant respectfully disagrees with, and explicitly traverses, the reason for rejecting the claims. However, in the interest of advancing the prosecution of this matter,

the claims have been amended to more clearly state the invention. More specifically, claims 1 and 13 have been amended to recite that the second memory is switched off. Similarly, claims 16-18 have been amended to depend from allowable claim 15.

Carmean discloses a method for maintaining cache coherency while minimizing power consumption. The method includes operating a first processor in a reduced power mode ..[of which] ... certain portion of the internal logic in the first processor remain clocked so that the first processor continues to monitor transactions of the system bus." (See Abstract). Carmean fails to disclose that the memory is switched-off.

It is well recognized that to constitute a rejection pursuant to 35 USC 102, i.e., anticipation, all material elements recited in a claim must be found in one unit of prior art. For the reason shown, Carmean cannot be said to anticipate the present invention, because Carmean fails to disclose each and every element recited in claim 1.

Having shown that Carmean fails to disclose each and every element recited in the claim, applicant submits that the reason for the rejection of the claim has been overcome and the rejection can no longer be sustained. Applicant respectfully requests entry of the withdrawal of the rejection and allowance of the claim.

With regard to claim 13, this claim recites data processing unit similar to that recited in claim 1 and has been amended in a manner similar to amendment made to claim 1. The claim has been rejected for the same reason used in rejecting claim 1. Thus, the applicant's remarks made in response to the rejection of claim 1 are also applicable in response to the rejection of claim 13. In view of the amendments made to claim 13 and for the remarks made with regard to the rejection of claim 1, which are repeated, as if in full, herein, in response to the rejection of claim 13, applicant submits that the reason for rejecting this claim has been overcome and the rejection can no longer be sustained. Applicant respectfully requests withdrawal of the rejection and allowance of the claims.

With regard to claims 2, 4, 5, 6, 14 and 16-18 these claims ultimately depend from claims 1, 13 and 15, which include subject matter not disclosed in, and, hence, allowable over, the cited reference. Accordingly, claims 2, 4, 5, 6, 14, and 16-18 are also allowable by virtue of their dependence from an allowable base claim. Applicant respectfully requests withdrawal of the rejection and allowance of the claims.

Claims 7 and 19 stand rejected under 35 USC § 103(a) as being unpatentable over Carmean in view of Conary (USP no. 5,481,731).

Applicant respectfully disagrees with, and explicitly traverses, the reason for rejecting the claims.

With regard to claim 7, this claim depends from claim 1, which has been shown to include subject matter not disclosed by Carmean, i.e., the memory being switched off. Conary discloses a method and system for allowing a processor to invalidate an individual line of its internal cache while in a non-clocked low power state. Conary teaches that the processor monitors the memory and when a change is written into the memory by another device, the processor is powered-up momentarily to alter a cache memory to correspond to the change made to the memory. However, Conary fails to teach or suggest that the memory is switched off.

Neither Carmean nor Conary, individually or in combination, teach or suggest all the elements recited in the above referred-to claims. From the teachings of Carmean and Conary, even if combined, one would not have been motivated to develop a system having all the features recited in the independent claim..

Having shown that the combination of Carmean and Conary fails to teach or suggest all the elements recited in claims 1/7, applicant submits that the reason for the rejection has been overcome and the rejection can no longer be sustained. Applicant respectfully requests withdrawal of the rejection and allowance of the claim.

With regard to claim 19, this claim has been amended to depend from claim 15, which has been indicated to be allowable. Accordingly, claim 19 is also allowable by virtue of its dependency from an allowable base claim. Applicant respectfully requests withdrawal of the rejection and allowance of the claims.

Claim 8-10 and 12 stand rejected under 35 USC § 102(e) as being anticipated by Pawate (USP no. 6,285,704).

Applicant respectfully disagrees with, and explicitly traverses, the reason for rejecting the claims. However, in the interest of advancing the prosecution of this matter,

claim 8 has been amended to more clearly state the invention. More specifically, claim 8 has been amended to recite that the second memory is switched off.

Pawate discloses a computer system including a main processing unit coupled to a DSP/memory module. Pawate discloses that when the main processing unit provides information to the DSP, the DSP is switched off. However, Pawate fails to disclose that the second memory is switched-off, as is recited in the claims.

It is well recognized that to constitute a rejection pursuant to 35 USC 102, i.e., anticipation, all material elements recited in a claim must be found in one unit of prior art. For the reason shown, Pawate cannot be said to anticipate the present invention, because Pawate fails to disclose each and every element recited in claim 1.

Having shown that Pawate fails to disclose each and every element recited in the claim, applicant submits that the reason for the rejection of claim 8 has been overcome and the rejection can no longer be sustained. Applicant respectfully requests withdrawal of the rejection and allowance of the claim.

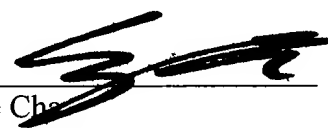
With regard to claims 9, 10 and 12 these claims ultimately depend from claim 8, which includes subject matter not disclosed in, and, hence, allowable over, the cited reference. Accordingly, claims 9, 10 and 12 are also allowable by virtue of their dependence from an allowable base claim. Applicant respectfully requests withdrawal of the rejection and allowance of the claim.

For all the foregoing reasons, it is respectfully submitted that all the present claims are patentable in view of the cited references. A Notice of Allowance is respectfully requested.

Respectfully submitted,

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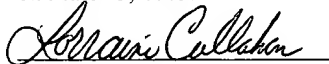
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